

United States District Court,  
S.D. California.

**HEWLETT-PACKARD DEVELOPMENT COMPANY, L.P.,**  
Plaintiff.

v.

**GATEWAY, INC.,**  
Defendant.

**Gateway, Inc.,**  
Counterclaim-Plaintiff.

v.

**Hewlett-Packard Development Company, L.P., Hewlett-Packard Company and Compaq Information Technologies Group, L.P.,**  
Counterclaim-Defendants.

Civil No. 04CV0613-B(LSP)

**Feb. 1, 2006.**

John Allcock, DLA Piper US, San Diego, CA, for Plaintiff.

Darryl J. Adams, Dean M. Munyon, James D. Smith, Wayne Harding, Dewey Ballantine, W. Bryan Farney, Dechert LLP, Austin, TX, Jonathan D. Baker, Dechert LLP, Mountain View, CA, for Defendant.

**CLAIM CONSTRUCTION ORDER FOR UNITED STATES PATENT NUMBER 6,609,211**

**RUDI M. BREWSTER, Senior District Judge.**

Pursuant to *Markman v. Westview Instruments, Inc.*, 517 U.S. 370 (1996), on January 11-12, 2006, the Court conducted a *Markman* hearing in the above-titled patent infringement action regarding construction of the disputed claim terms for U.S. Patent Number 6,609,211 ("the '211 patent"). Plaintiff Hewlett-Packard Development Company, L.P. ("HP") was represented by the law firm of DLA Piper Rudnick Gray Cary U.S. LLP, Defendant Gateway, Inc. ("Gateway") was represented by the law firm of Dewey Ballantine LLP.

At the *Markman* hearing, the Court, with the assistance of the parties, analyzed the claim terms in order to prepare jury instructions interpreting the pertinent claims at issue in the '211 patent. Additionally, the Court prepared a case glossary for terms found in the claims and the specification for the '211 patent considered to be technical in nature which a jury of laypersons might not understand clearly without specific definition.

After careful consideration of the parties' arguments and the applicable statutes and case law, the Court **HEREBY CONSTRUES** the claims in dispute in the '211 patent and **ISSUES** the relevant jury instructions as written in Exhibit A, attached hereto. Further, the Court **HEREBY DEFINES** all pertinent technical terms as written in Exhibit B, attached hereto.

**IT IS SO ORDERED.**

*EXHIBIT A FN1*

FN1. All terms appearing in bold face type and underlined have been construed by the court and appear with their definitions in the glossary in Exhibit B. The definition for each construed term appears in italics after its first use in the patent.

**UNITED STATES PATENT NUMBER 6.609,211 B2-CLAIM CHART**

<b>VERBATIM CLAIM LANGUAGE</b>	<b>COURT'S CONSTRUCTION</b>
<b>Claim 1</b>	
1. A method of power management, comprising the steps of:	1. A method of power management, comprising the steps of:
measuring hardware activity of a clocked device; and	<b>measuring</b> [ <i>determining the quantity of</i> ] <b>hardware activity</b> [ <i>electrical events</i> ] of a <b>clocked device</b> [ <i>a device that operates under the control of a timing signal</i> ]; and
determining utilization of the clocked device based on the hardware activity of the clocked device over a period of time; and	determining utilization of the <b>clocked device</b> based on the <b>hardware activity</b> of the <b>clocked device</b> over a period of time; and
adjusting the clocked device from a first power consumption mode to a second power consumption mode based on the utilization of the clocked device.	adjusting the <b>clocked device</b> from a first power consumption mode to a second power consumption mode based on the utilization of the <b>clocked device</b> .
<b>Claim 2</b>	
2. The method of claim 1, the adjusting step comprising the step of:	2. The method of claim 1, the adjusting step comprising the step of:
reducing the clocked device from the first power consumption mode to the second clocked consumption mode.	<b>reducing</b> [ <i>lowering</i> ] the <b>clocked device</b> from the first power consumption mode to the second clocked consumption mode.
<b>Claim 3</b>	
3. The method of claim 1, the adjusting step comprising the step of:	3. The method of claim 1, the adjusting step comprising the step of:
increasing the clocked device from the first power consumption mode to the second power consumption mode.	<b>increasing</b> [ <i>raises</i> ] the <b>clocked device</b> from the first power consumption mode to the second power consumption mode.
<b>Claim 4</b>	
4. The method of claim 1, wherein the first power consumption mode and the second power consumption mode are two of a plurality of selectable power consumption modes.	4. The method of claim 1, wherein the first power consumption mode and the second power consumption mode are two of a <b>plurality</b> [ <i>two or more</i> ] of selectable power consumption modes.
<b>Claim 5</b>	
5. The method of claim 1, wherein the adjusting step is performed during active times of the clocked device.	5. The method of claim 1, wherein the adjusting step is performed during active times of the <b>clocked device</b> .
<b>Claim 6</b>	
6. The method of claim 1, wherein the hardware activity indicates inactive times of the clocked device.	6. The method of claim 1, wherein the <b>hardware activity</b> indicates inactive times of the <b>clocked device</b> .
<b>Claim 7</b>	
7. The method of claim 1, the measuring step comprising the step of:	7. The method of claim 1, the measuring step comprising the step of:
monitoring the hardware activity of the clocked device using an activity counter.	monitoring the <b>hardware activity</b> of the <b>clocked device</b> using an activity <b>counter</b> [ <i>hardware used to count electrical events</i> ].
<b>Claim 8</b>	

8. The method of claim 7, the determining step comprising the steps of: reading an activity count of the activity counter; and comparing the activity count to a value.	8. The method of claim 7, the determining step comprising the steps of: reading an activity count of the activity <i>counter</i> ; and comparing the activity count to a value.
<b>Claim 9</b>	
9. The method of claim 8, further comprising the step of: selectively adjusting the clocked device from a first power consumption mode to a second power consumption mode based on the comparing step.	9. The method of claim 8, further comprising the step of: selectively adjusting the <i>clocked device</i> from a first power consumption mode to a second power consumption mode based on the comparing step.

**EXHIBIT B**

UNITED STATES PATENT NUMBER 6,609,211 **B2-GLOSSARY OF TERMS**

<b>TERM</b>	<b>DEFINITION</b>
<b>Clocked device</b>	a device that operates under the control of a timing signal
<b>Counter</b>	hardware used to count electrical events
<b>Hardware activity</b>	electrical events
<b>Increasing</b>	raising
<b>Measuring</b>	determining the quantity of
<b>Plurality</b>	two or more
<b>Reducing</b>	lowering

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